

Form PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY DOCKET NO. 0325.00417	SERIAL NO. 09/668,801
O I P E INFORMATION DISCLOSURE STATEMENT BY APPLICANT JUL 24 2001 O I P E	APPLICANT: Timothy J. Williams	GROUP 2188
	FILING DATE September 22, 2000	

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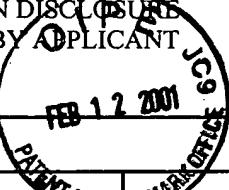
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C	4	0	6	1	9	8	7	12/6/1977	Nagahama	331	111		
	4	2	7	2	7	6	0	6/9/1981	Prazak et al.	340	347		
	4	3	4	4	0	6	7	8/10/1982	Lee	340	347		
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	4	8	6	8	5	2	5	9/19/1989	Dias	331	111		
	4	9	4	7	1	6	9	8/7/1990	Smith et al.	341	121		
	5	1	4	0	1	9	7	8/18/1992	Grider	307	480		
	5	1	5	0	0	7	9	9/22/1992	Williams et al.	331	75		
	5	1	7	5	8	8	4	12/29/1992	Suarez	455	260		
	5	2	0	0	7	5	1	4/6/1993	Smith	341	147		
	5	3	0	4	9	5	5	4/19/1994	Atriss et al.	331	1R		
C	5	3	1	9	3	7	0	6/7/1994	Signore et al.	341	120		
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q	WP 3.5: An Integrated Time Reference, By: Robert A. Blauschild, ISSCC94/Session 3, Analog Techniques/Paper WP 3.5, 1994.												
C	Micropower CMOS Temperature Sensor with Digital Output, By: Anton Bakker et al., 1996 IEEE.												
a	CY7C63722/23 CY7C63742/43 enCoRe™ USB Combination Low-Speed USB & PS/2 Peripheral Controller, Cypress Semiconductor Corp., February 2000 - Revised April 11, 2000, pages 1-51.												
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1	5	4	4	0	3	0	5	8/8/1995	Signore et al.	341	120
	5	5	4	6	4	3	3	8/13/1996	Tran et al.	375	376
	5	5	5	2	7	4	8	9/3/1996	O'Shaughnessy	331	1A
	5	5	6	3	5	5	3	10/8/1996	Jackson	331	57
	5	5	6	5	8	1	9	10/15/1996	Cooper	331	111
	5	5	8	3	5	0	1	12/10/1996	Henrion et al.	341	118
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	5	6	0	4	4	6	6	2/18/1997	Dreps et al.	331	113R
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C	An Analog PLL-Based Clock and Data Recovery Circuit with High Input Jitter Tolerance, By: Sam Yinshang Sun, Reprinted from IEEE Journal of Solid-State Circuits, Vol. SC-24, pp. 325-330, April 1989.
C	CY7C63221/31 enCoRe™ USB Low Speed USB Peripheral Controller, Cypress Semiconductor Corp., February 2000 - Revised April 11, 2000, pages 1-43.
C	Timothy J. Williams et al., "AUTO-LOCKING OSCILLATOR FOR DATA COMMUNICATIONS", U.S. Serial No. 09/511,019, Filed February 23, 2000.
C	Timothy J. Williams et al., "CIRCUIT FOR LOCKING AN OSCILLATOR TO A DATA STREAM", U.S. Serial No. 09/511,020, Filed February 23, 2000.

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	5 7 2 6 5 9 7	3/10/1998	Petty et al.	327	307	
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C	Universal Serial Bus Specification, Revision 2.0, April 27, 2000, pp. 1-622.
C	Monte F. Mar et al., "PROGRAMMABLE OSCILLATOR SCHEME", U.S. Serial No. 09/721,316, Filed November 22, 2000.
C	Monte F. Mar et al., "PROGRAMMABLE OSCILLATOR SCHEME", U.S. Serial No. 09/275,336, Filed March 24, 1999.

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